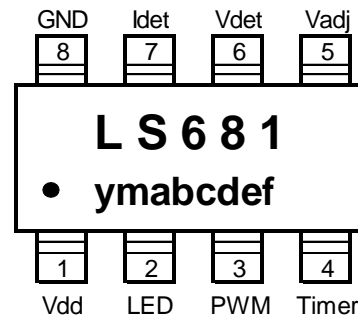


■ Features

- ◆ Build-in high accuracy A/D convertor for detecting charge full conditions of Vfull.
- ◆ Noise filter logic design for reducing PCB's noise to meet high accuracy data collection.
- ◆ For 2 or multi cells NiMH battery charge application.
- ◆ Battery alive function for waking-up long time no use battery, which battery voltage is so low as no battery.
- ◆ Charge current is settable (by external Resistor); and Pre/ Fast current are automatically adjusted for different battery voltage level.
- ◆ 2 Charge full terminations
 - Battery voltage termination can be set to meet over 90% full capacity.
 - Timer termination: force to stop when timeout, for some minor portion of batteries whose full battery voltage is too low.
- ◆ Charge time out protect.
- ◆ Charge status indication by Single color LED for Power on/ Charge/ Full/ Defect.
- ◆ Re-start charge process for battery voltage drop by self-discharged.
- ◆ Working voltage: 4.5V.
- ◆ Marking: LS681
ymabcdef
ym: year / month.
abcdef: Wafer tracking code.
- ◆ Package: SOP-8 (150 mil)



■ Pin Assignment

Pin No.	Name	I/O	Description
1	Vdd	P	Power Input (4.5V)
2	LED	O	LED charge status output
3	PWM	O	Charge loop switching control output
4	Timer	I	Charge time out select input
5	Vadj	I	Full voltage fine tune adjust input
6	Vdet	I	Battery voltage detect input
7	Idet	I	Charge current setting/detect input
8	GND	P	Ground